

## Electronic Acknowledgement Receipt

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<b>Application Number:</b>	10707206
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	1205
<b>Title of Invention:</b>	OPTIMIZED CONDUCTIVE LID MOUNTING FOR INTEGRATED CIRCUIT CHIP CARRIERS
<b>First Named Inventor/Applicant Name:</b>	Michael A. Gaynes
<b>Customer Number:</b>	24241
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<b>Filer Authorized By:</b>	Anthony J. Canale
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	FR920030002US1IDS.pdf	178771	no	3

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<b>Information:</b>					
This is not an USPTO supplied IDS fillable form					
2	Foreign Reference	FR920030002US1JP043352.pdf	72043	no	1
<b>Warnings:</b>					
<b>Information:</b>					
3	Foreign Reference	FR920030002US1JP114413.pdf	70462	no	1
<b>Warnings:</b>					
<b>Information:</b>					
4	Foreign Reference	FR920030002US1JP145333.pdf	60869	no	1
<b>Warnings:</b>					
<b>Information:</b>					
5	Foreign Reference	FR920030002US1JP293808.pdf	64983	no	1
<b>Warnings:</b>					
<b>Information:</b>					
6	Foreign Reference	FR920030002US1144971.pdf	59770	no	1
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<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			506898		
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